◆ Polaroid.

Service Manual





Functional Board List LCD/Plasma Televisions

Please note that this BOM list may vary from the original documentation. This part list supersedes the parts list contained within the body of the service manual. Please reference the part numbers below when ordering replacement boards of the servicing of this model.

If you require additional technical support, please contact our Tech Support line at **1-866-396-6322**

Model	Part Number	Description	Boards
	667-L32K5-05	Keypress Board	1
	667-L32K5-09	IR Receive Board	1
	667-L32K5-16	USB Board	1
FLM-3201	667-L32K5N-40	Audio/Video Processing Board	1
FLM-32U1	667-L32K5N-56	CPU Board	1
	667-L34K5-20	Power Supply Board KAS20	1
	301-DL26K7-01A	Remote RC-D01-0A	1
	615-10464-06	Stand Assy	1

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Attention: This service manual is only for service personnel to take reference with. Before servicing please read the following points carefully.

Safety instructions

1. Instructions

Be sure to switch off the power supply before replacing or welding any components or inserting/plugging in connection wire Anti static measures to be taken (throughout the entire production process!):

- a) Do not touch here and there by hand at will;
- b) Be sure to use anti static electric iron;
- c) It's a must for the welder to wear anti static gloves.

Please refer to the detailed list before replacing components that have special safety requirements. Do not change the specs and type at will.

2. Points for attention in servicing of LCD

- 2.1 Screens are different from one model to another and therefore not interchangeable. Be sure to use the screen of the original model for replacement.
- 2.2 The operation voltage of LCD screen is 700-825V. Be sure to take proper measures in protecting yourself and the machine when testing the system in the course of normal operation or right after the power is switched off. Please do not touch the circuit or the metal part of the module that is in operation mode. Relevant operation is possible only one minute after the power is switched off.
- 2.3 Do not use any adapter that is not identical with the TV set. Otherwise it will cause fire or damage to the set.
- 2.4 Never operate the set or do any installation work in bad environment such as wet bathroom, laundry, kitchen, or nearby fire source, heating equipment and devices or exposure to sunlight etc. Otherwise bad effect will result.
- 2.5 If any foreign substance such as water, liquid, metal slices or other matters happens to fall into the module, be sure to cut the power off immediately and do not move anything on the module lest it should cause fire or electric shock due to contact with the high voltage or short circuit.
- 2.6 Should there be smoke, abnormal smell or sound from the module, please shut the power off at once. Likewise, if the screen is not working after the power is on or in the course of operation, the power must be cut off immediately and no more operation is allowed under the same condition.
- 2.7 Do not pull out or plug in the connection wire when the module is in operation or just after the power is off because in this case relatively high voltage still remains in the capacitor of the driving circuit. Please wait at least one minute before the pulling out or plugging in the connection wire.
- 2.8 When operating or installing LCD please don't subject the LCD components to bending, twisting or extrusion, collision lest mishap should result.
- 2.9 As most of the circuitry in LCD TV set is composed of CMOS integrated circuits, it's necessary to pay attention to anti statics. Before servicing LCD TV make sure to take anti static measure and ensure full grounding for all the parts that have to be grounded.
- 2.10 There are lots of connection wires between parts behind the LCD screen. When servicing or moving the set please take care not to touch or scratch them. Once they are damaged the screen

would be unable to work and no way to get it repaired.

- 2.11 Special care must be taken in transporting or handling it. Exquisite shock vibration may lead to breakage of screen glass or damage to driving circuit. Therefore it must be packed in a strong case before the transportation or handling.
- 2.12 For the storage make sure to put it in a place where the environment can be controlled so as to prevent the temperature and humidity from exceeding the limits as specified in the manual. For prolonged storage, it is necessary to house it in an anti-moisture bag and put them altogether in one place. The ambient conditions are tabulated as follows:

Temperature	Scope for operation	0 ~ +50 °C
	Scope for storage -20 ~ +60 °C	
humidity	Scope for operation	20% ~ 85%
	Scope for storage	10% ~ 90%

2.13 Display of a fixed picture for a long time may result in appearance of picture residue on the screen, as commonly called "ghost shadow". The extent of the residual picture varies with the maker of LCD screen. This phenomenon doesn't represent failure. This "ghost shadow" may remain in the picture for a period of time (several minutes). But when operating it please avoid displaying still picture in high brightness for a long time.

3. Points for attention during installation

- 3.1 The front panel of LCD screen is of glass. When installing it please make sure to put it in place.
- 3.2 For service or installation it's necessary to use specified screw lest it should damage the screen.
- 3.3 Be sure to take anti dust measures. Any foreign substance that happens to fall down between the screen and the glass will affect the receiving and viewing effect
- 3.4 When dismantling or mounting the protective partition plate that is used for anti vibration and insulation please take care to keep it in intactness so as to avoid hidden trouble.
- 3.5 Be sure to protect the cabinet from damage or scratch during service, dismantling or mounting.

Alignment instruction

1 Alignment equipment

PM5518 (video signal generator)

K-7253 (VGA signal generator)

CA210 (white balancer)

2 Alignment flow-chart

The alignment flow-chart is shown as fig-1

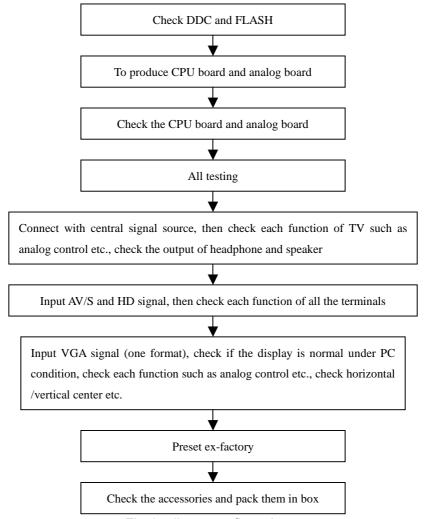


Fig-1 adjustment flow-chart

3 Unit adjustment

Connect CPU board and analog board according to wiring diagram of 203-L32K50-03JL, connect with power and observe the display.

Method for entering factory menu: press "VOL+", "MUTE" and "VIDEO" repeatedly to enter factory menu; press "ENTER" to select different items when the first line of each adjustment item just lights up; input VGA and DVI signal, then select the "mode" item; after that, you can press "enter" to select three kinds of color temperature namely 6500K, 9300K and 12000K press "MENU" to exit.

Note: the white balance adjustment should be done under "nature" picture mode.

3.1 EEPROM initialization

Enter the first page of factory menu, select "clear eeprom" and then press "enter", shut down the unit after "ok" appears.

3.2 VGA/DVI channel adjustment

3.2.1 Preset VGA channel mode

Input VGA signal (PATTERN 5: Final Test) of K-7253, select TIME301(640*350/70Hz), press "AUTO" to do the auto adjustment until the screen is filled with picture. Use the same method to do auto adjustment for the following items:

TIME302 (720*400/70Hz)	TIME303 (640*480/60Hz)	TIME311 (800*600/60Hz)
TIME313 (1024*768/60Hz)	TIME315 (640*480/75Hz)	TIME316 (800*600/75Hz)
TIME317 (1024*768/75Hz)	TIME319 (1280*1024/75Hz)	TIME339 (1280*1024/60Hz)

3.2.2 ADC adjustment of VGA channel

Adjust ADC-gain to 15.

3.2.3 White balance adjustment VGA/DVI channel

Input K-7253 8 level gray-scale signal of TIME311 and PATTERN474, enter white balance adjustment menu; adjust the third and seventh levels using white balance.

Select 6500k of "mode", adjustment offset_R, offset_G and offset_B, let the color coordinate of the third level be 308 and 316 and its brightness be 16nit more or less. Adjust gain_R, gain_G and gain_B, let the color coordinate of seventh level be 308 and 316. Adjustment offset_R, offset_G, offset_B, gain_R, gain_G and gain_B repeatedly until the value of the two levels gray-scale be 308 and 316.

Select 9300k of "mode", adjustment offset_R,offset_G and offset_B,let the color coordinate of the third level be 285 and 290 and its brightness be 16nit more or less. Adjust gain_R,gain_G and gain B, let the color coordinate of seventh level be 285 and 290.

Select 12000k of "mode", adjustment offset_R, offset_G and offset_B, let the color coordinate of the third level be 270 and 283 and its brightness be 16nit more or less. Adjustment offset_R, offset_G, offset_B, gain_R, gain_G and gain_B repeatedly until the value of the two levels gray-scale be 270 and 283.

Note: gain_R, gain_G, gain_B is value not above 128 and let its value 128 at least.

3.3 Adjustment TV channel

3.3.1 Adjustment VCO, OPTION, sub-brightness and sub-contrast

Input AV color bar signal (PM5518 COLOR BAR 100%) to VIDEO 1 terminal, enter the first page of factory menu, press "enter" selecting "auto color", display "OK" after 2 seconds; then you can finish the VCO adjustment; set the value of "option" to 10, "Hor-width" to 32 and "s-bright" to 140 as well as S-contrast to 140. set the MaxVolume according to its product standard.

3.3.2 white balance adjustment of TV channel

Input AV signal (PM5518, NTSC system, 8 level gray-scale signal), enter adjustment menu of white balance, adjust the third level and seventh level using white balance. Adjust offset_R, offset_G and offset_B to let color coordinate of the third level be 270 and 275 and its brightness be about 21nit. fixate gain_B to 128, adjust gain_R, gain_G to color coordinate of the seventh level be 280 and 280. adjust offset_R, offset_B, gain_R and gain_G, repeatedly using the same method until

the value of the two levels gray-scale be the specified value.

3.4 white balance adjustment YPbPr channel

input YpbPr signal of K-7253 to YPbPr-1 terminal, input TIME380(480i) PATTERN471 8 level gray-scale signal, set the value of Hor-width to 23, Ver-width to 5, S-bright to 120 and S-contrast to 140.Enter adjustment menu of white balance, adjust the third level using white balancer. Adjust offset_R, offset_G and offset_B, to let color coordinate of the third level be 270 and 275 and its brightness be about 15nit.

Input 8 level gray-scale signal of TIME392(480p), TIME394(720p) and TIME396(1080i) separately, repeat the above operations to let color coordinate of the third level be 270 and 275 and its brightness be about 15nit.

4 Performance check

4.1 TV function

Enter searching menu → auto search, connect RF-TV terminal with central signal source and check if there are channels be skipped

4.2 AV/S, YpbPr terminals

Input AV/S, HD signal, check if it is normal.

4.3 VGA terminal

Insert VGA terminal, input VGA format signal of 640 X 480@60 Hz, check if the display is normal. If interference exists, press the auto adjust button on remote control again and check if it is normal.

4.4 DVI terminal

Insert DVI terminal, input signal of 640 X 480@60 Hz signal and check if the display is normal.

4.5 check sound channel

Check the speaker and headphone of each channel.

4.6 presetting before ex-factory

item	setting
Picture mode	NATURE
Sound mode	NEWS
N/R	WEAK
SCREEN	16:9

item	setting
OSD language	English
VGA color	9300
temperature	
SPEAKER	ON
HEAD PHONE	ON

item	setting
BALANCE	50
SRS	OFF
CCD	OFF
Turn off	TV

Trouble shooting

Before servicing please check to find the possible causes of the troubles according to the table below.

1.Antenna(signal):

Picture is out of focus or jumping	•	Bad status in signal receiving
	•	Poor signal
	•	Check if there are failures with the electrical connector or
		the antenna.
	•	Check if the antenna is properly connected.

Fringe in picture	Check if the antenna is correctly oriented.
	Maybe there is electric wave reflected from hilltop or
	building.
Picture is interfered by stripe	Possibly due to interference from automobile, train, high
shaped bright spots	voltage transmission line, neon lamp etc.
	Maybe there is interference between antenna and power
	supply line. Please try to separate them in a longer
	distance.
	Maybe the shielded-layer of signal wire is not connected
	properly to the connector.
There appear streaks or light color	Check if interfered by other equipment and if interfered
on the screen	possibly by the equipment like transmitting antenna, non
	professional radio station and cellular phone.

2.TV set:

Symptoms	Possible cause
Unable to switch the power on	 Check to see if the power plug has been inserted properly into the socket.
No picture and sound	 Check to see if the power supply of liquid crystal TV has been switched on. (as can be indicated by the red LED at the front of the TV set) See if it's receiving the signal that is transmitted from other source than the station Check if it's connected to the wrong terminal or if the input mode is correct. Check if the signal cable connection between video frequency source and the liquid crystal TV set is correct.
Deterioration of color phase or color tone	Check if all the picture setups have been corrected.
Screen position or size is not proper	Check is the screen position and size is correctly set up.
Picture is twisted and deformed	Check to see if the picture-frame ratio is properly set up.
Picture color changed or colorless	Check the "Component" or "RGB" settings of the liquid crystal TV set and make proper adjustment according to the signal types.
Picture too bright and there is distortion in the brightest area	 Check if the contrast setting is too high. Possibly the output quality of DVD broadcaster is set too high. It maybe also due to improper terminal connection of the video frequency signal in a certain position of the system.
Picture is whitish or too bright in the	Check if the setting for the brightness is too high

darkest area of the picture	Possibly the brightness grade of DVD player (broad-acts) is act to a bight.			
	(broadcaster) is set too high.			
No picture or signal produced from the	Check if the cable is disconnected.			
displayer if "XXX in search" appears.	Check if it's connected to the proper terminal or if the			
	input mode is correct.			
There appears an indication - "outside	Check if the TV set can receive input signal. The			
the receivable scope)	signal is not correctly identified and VGA format is			
	beyond the specified scope.			
Remote control cannot work properly	Check if the batteries are installed in the reverse			
	order.			
	Check if the battery is effective.			
	Check the distance or angle from the monitor.			
	Check if there is any obstruct between the remote			
	control and the TV set.			
	 Check if the remote control signal- receiving window 			
	is exposed to strong fluorescence.			
No picture and sound, but only hash.	Check if the antenna cable is correctly connected, or			
	if it has received the video signal correctly.			
Blur picture	Check if the antenna cable is correctly connected.			
	Of if it has received the right video signal.			
No sound	Check if the "mute" audio frequency setting is			
	selected.			
	Check if the sound volume is set to minimum.			
	Make sure the earphone is not connected.			
	Check if the cable connection is loose.			
When playing VHS picture search	When being played or in pause VHS picture search			
tape, there are lines at the top or	tape sometimes can't provide stable picture, which			
bottom of the picture.	may lead to incorrect display of the liquid crystal TV,			
	In this case please press "auto" key on the remote			
	control so as to enable the liquid crystal TV set to			
	recheck the signal and then to display correct picture			
	signal			

Method of software upgrading

Steps of software upgrading are as follows:

- 1. Select a serial connection wire and a VGA connection wire and then connect them by means of a patch panel;
- 2. Use a serial wire to connect the PC to the patch panel and set TV set to off state; Open the software upgrade file holder and double click



FlashUpgraderNT (use under window 2000/XP/NT)



FlashUpgrader (use under window 98),

The following interfaces will show up after running the program:



Based on the computer features, set up the serial port (COM Port). Select corresponding serial port (if it's unable to FLASH WRITE, change to another port). Baud is selected to be 115200. Then select Reset Target After Download. Click FLASH pushbutton, it's ready to run. For other settings, please refer to the Fig. Above (already defaulted by the system, normally no need to change). Switch on TV set the FLASH write program begins to run;



After FLASH write is over, push button "cancel" will become flash. Then shut the main power supply and it's OK just switch it on again.

Note: Do not shut the power off or turn the TV set on during the FLASH write. Otherwise it may lead to no way for flash to rewrite.

LC-32K5 working principle analysis

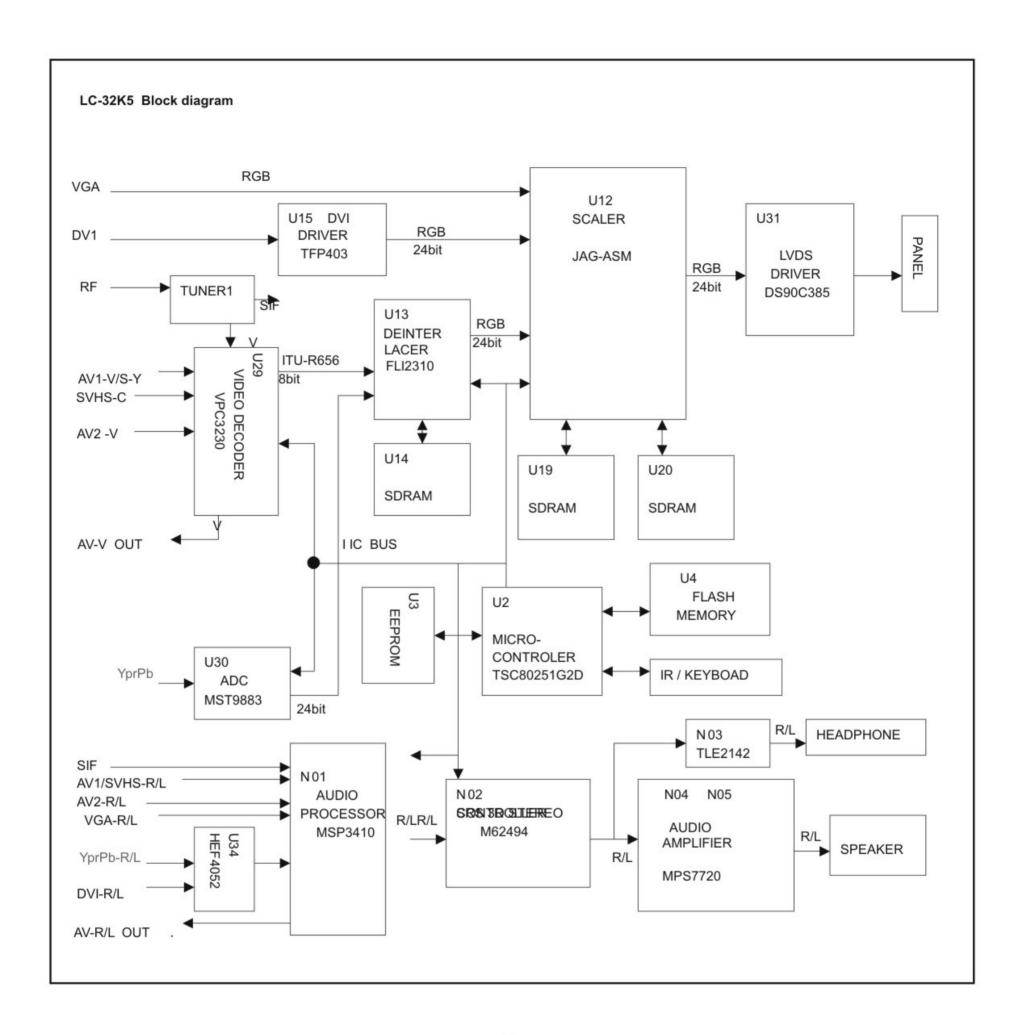
Please refer to LC-32K5 BLOCK DIAGRAM in order to know the working principle of LC-32K5 brief introduction to its signal flow is shown as follows:

RF signal produces composite color television signal through TUNER201, the signal together with AV1/SVHS(SVHS priority) and AV2 etc. are sent to U29 VPC3230D to decode and do switching selection. In addition to decode the selected video signal, part of the selected video signal is also sent to AV terminal and 21 line decoder U32 Z86229 which is exclusive to decode CCD/V-CHIP to decode. After decoded by U32, the signal will be sent back to U29 in the form of R G B and FB, then it will be added to the decoded picture and then it will be sent to U13 FL12310 in the form of ITU-R656 signal format of 8BIT to do line-by-line process.

Send two ways YPRPB signal to U30 MST9883 through selection by N2 PI5V330 in order to do the A/D conversion. After this, the signal will be converted into YUV signal of 24BIT, send it to FLI2310 also.

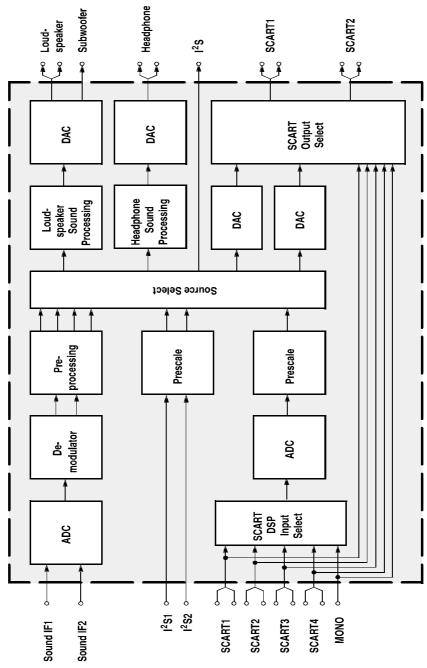
After a series of procedure such as matrix, chroma and tint etc. by FL12310, the two ways signal will be converted into RGB of 24bit, then send it to the main processing chip U12 JAG-ASM. send RGB signal it to the main processing chip U12 at the same time. There has another signal which should be sent to U12 together with he above two kinds of signal that is: RGB signal of 24BIT produced by DV1 through U15 TFP403/501. Do the picture format processing for the three kinds of signal in U12. Firstly do the A/D conversion for RGB signal of VGA, then do the switching selection together with the other two kinds of signal. And then they will be converted into 1024*768 format through digital display processing such as OSD and GAMMA correction etc. output it in the form of 24BIT RGB signal. Send it to LVDS convertor U31 DS90C383A, then it will be converted into signal format which can be accepted by LCD screen namely 3 low-voltage differential signal and 1 clock signal, finally send it to LCD screen to do the picture display PWM of U12 can also be used to control the brightness of back light-source.

Send SIF (the second IF) outputted by tuner to audio processor N201 MSP3420; send YPRPB and audio signal of DVI to N201 through switching selection by U34 HEF4052; Send AV1/SVHS, AV2 and audio signal of VGA to N201 also. Firstly SIF will be done the switching selection together with another four ways of audio signal, finally volume control and sound effect processing will be done. Output left-right sound channel signal R/L. one will be sent to SRS sound field processor N203 M62494 to do SRS processing and finally amplified by N213 and N214 MP7720 to drive the speaker, one will be used to drive the headphone through N212 TLE2142 and the last part will be outputted as R/L of AV OUT.



IC block diagram

1.MSP3420



Pins description:

2,3 PIN: SCL,SDA applied for control the operation of IC.

27,28 PIN: output left and right sound channel R/L to speaker processor.

36,37 PIN:AV OUT of sound R/L.

47,48 PIN:D4-1/D4-2/PCMCIA selected input R/L.

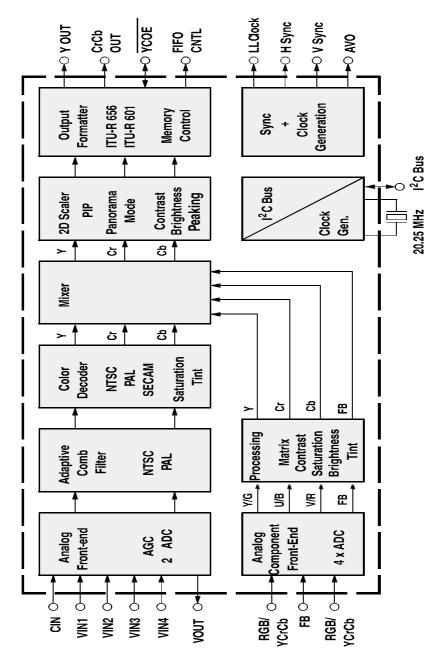
50,51 PIN:Input of VGA. R/L

53,54 PIN:Input of AV1/SVHS and AV2 R/L.

56,57 PIN:Input R/L of BS

67 PIN: Input TV SIF.

2.VPC3230



Pins description:

4 PIN: Input U of PCMCIA.

5,73 PIN: Input Y of PCMCIA.

6 PIN: Input V of PCMCIA

31-34, 37-40 PIN: output format signal of. ITU-R656

70 PIN:Video of AV OUT

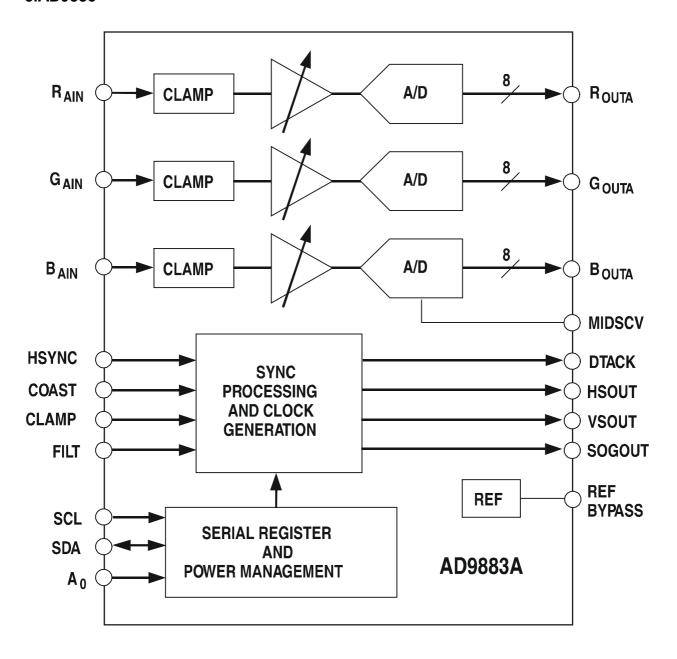
71 PIN:Input C of SVHS.

72 PIN:input Video (SVHS in priority) of Y or AV1 of SVHS.

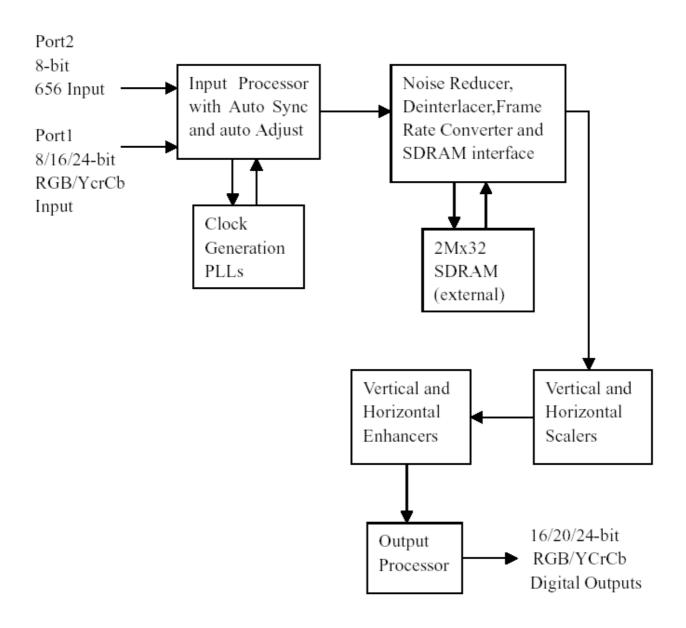
74 PIN:input Video of AV2.

75 PIN:input Video of TV/BS.

3.AD9883



4.FLI2310-Simplified Internal Block Diagram



5.JAG-ASM

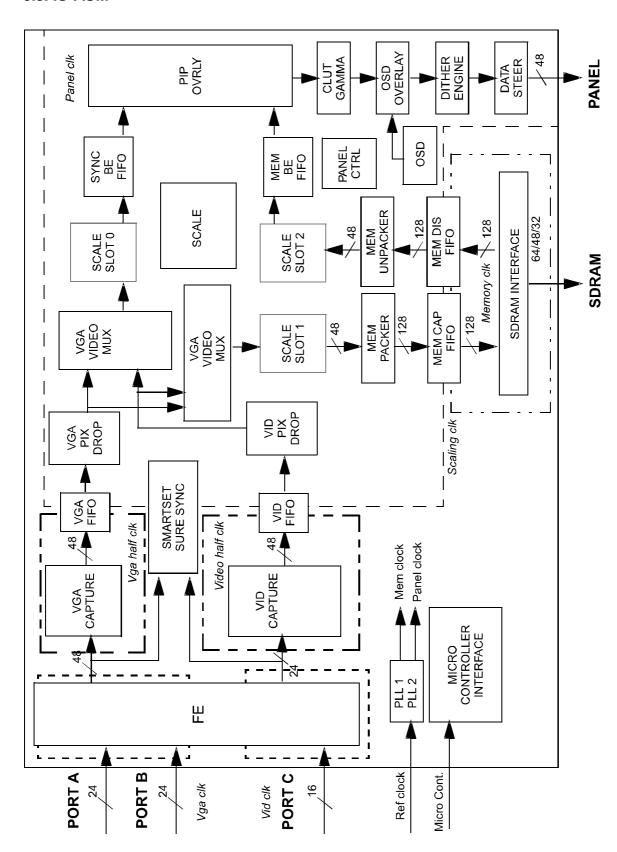


Figure 2. Functional Block Diagram

6.Z86229

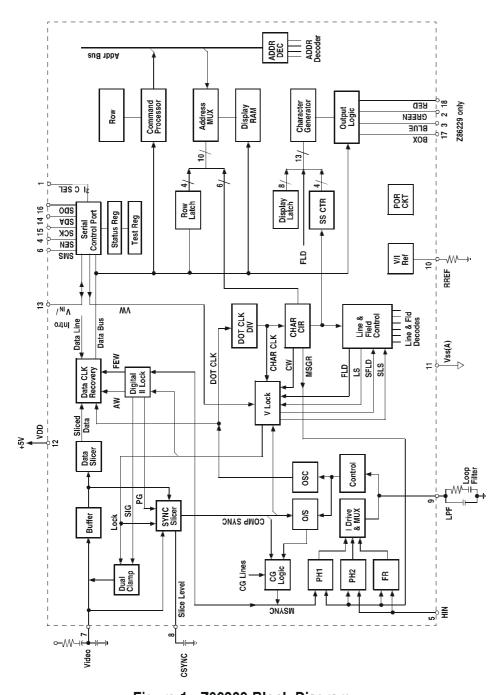


Figure 1. Z86229 Block Diagram

Pins description: 7 PIN: VDIEO INPUT 2 PIN: G OUTPUT 3 PIN: B OUTPUT 18 PIN: R OUTPUT 17 PIN: FB OUTPUT

Main assembly

NAME	NO.	N	IAIN COMPONENT AND NO.
AV processing board	667-L32K5N-40	N201 N202 N204 N205	MSP3410 (353-34100-80) M62494FP (353-62494-20) MPS7720 (353-77200-10)
CPU board	667-L32K5N-56	U12 U13 U15 U29 U30 U31	JAG-ASM (353-0JAG0-00) FLI2310 (353-23100-00) TFP501 (353-05010-00) VPC3230D (353-32300-80) MST9883B (353-98830-10) DS90C385 (353-03850-20) TSC80251G2D (353-80251-10) AT49F002NT (352-49002-70)
IR board	667-L32K5-09	1	
Keypad board	667-L32K5-05		
USB board	667-L32K5-16		
Power supply board	667-L23K7-20		
Remote control (RC-U38R)	301-UL27K6-38R		
Panel (LTA320W2-L01)	335-32001-00		

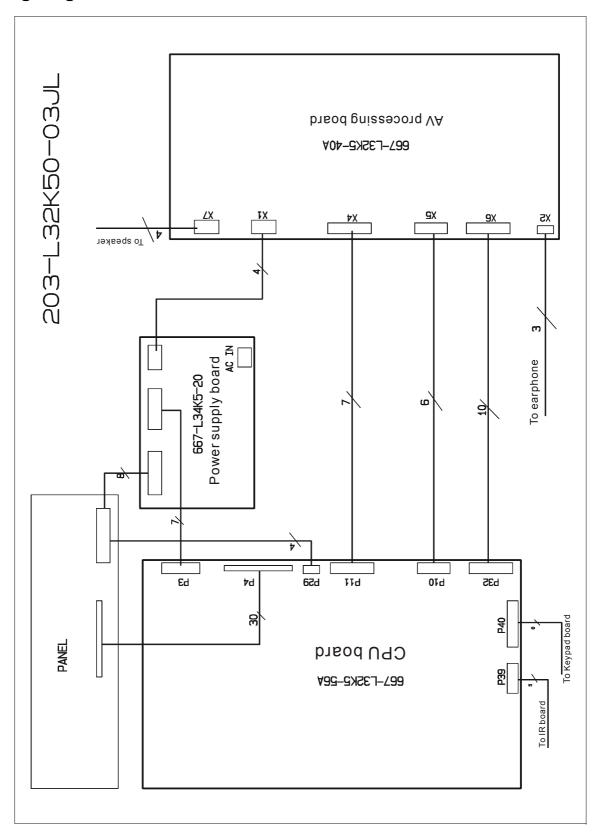
Identification criteria for the bright spot and dark spot of the LCD screen

Category criteria			Q	ty allow	ed		Distance between two spots				
Category	Cillella	15"	20"	22"	30"	40"	15"	20"	22"	30"	40"
Bright spot	One single spot	≤5	≤2	≤5	≤2	≤3		≥15mm			
	2 neighboring spots	≤2	≤1	≤2	≤1	≤1					
	Total No.	≤5	≤2	≤5	≤2	≤3					
	One single spot	≤6	≤7	≤5	≤4	≤10	≥15mm				
Dark	Two							≥10mm		≥5mn	,
spots	neighboring	≤2	≤2	≤2	≤1	≤5		210111111		2311111	1
	spots										
	Total No.	≤6	≤7	≤5	≤4	≤10					
Total defected point		≤8	≤7	≤5	≤4	1					

Notes:

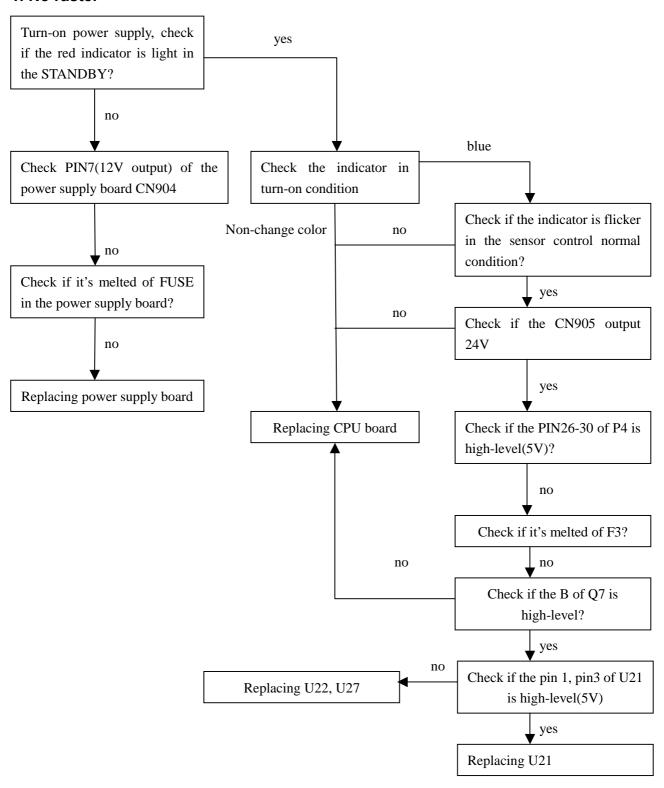
- 1. Definition of defected point (bright spot, dark spot): It is identified as a defected point if its area exceeds 1/2 of a single picture element (R,G,B).
- 2. Definition of bright spot: It is identified as a bright spot if it is bright in the state of dark field and its bright size remains unchanged
- 3. Definition of dark spot: It is identified as a dark spot if it is dark in the state of white field and its dark size remains unchanged
- 4. Definition of two neighboring points: Defects of a group of picture elements(RB,RG,GB).

Wiring diagram

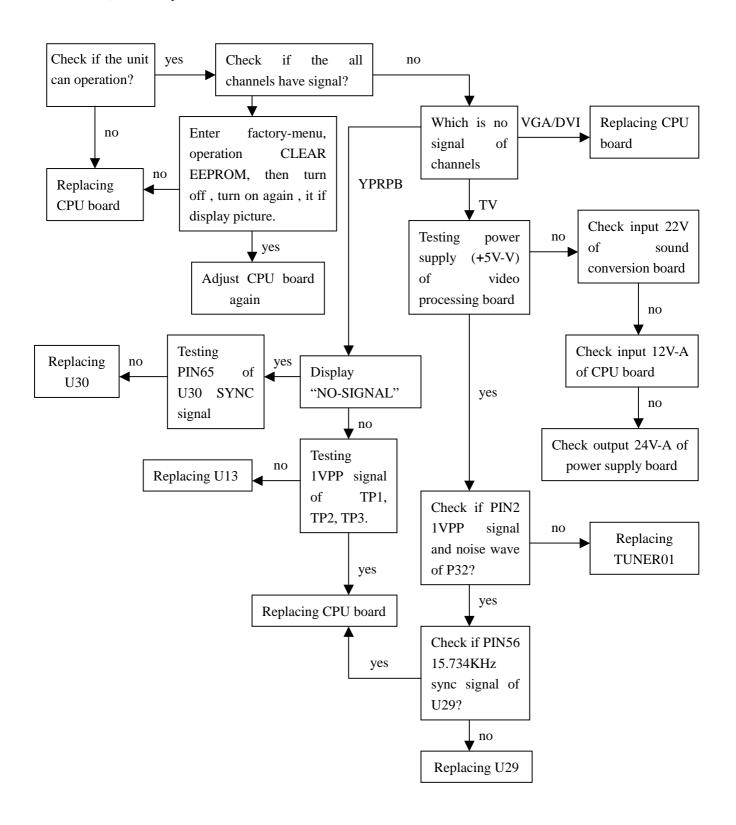


Troubleshooting charts

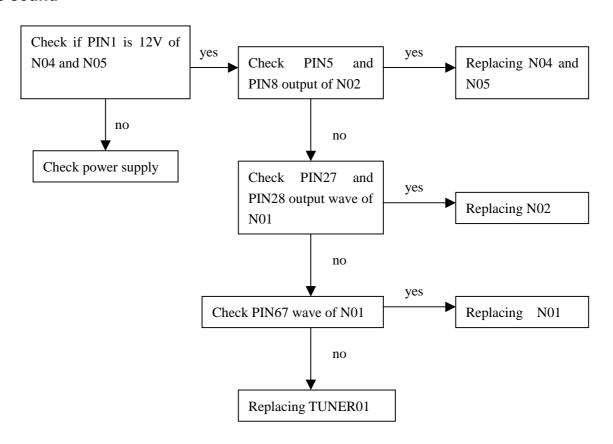
1. No raster

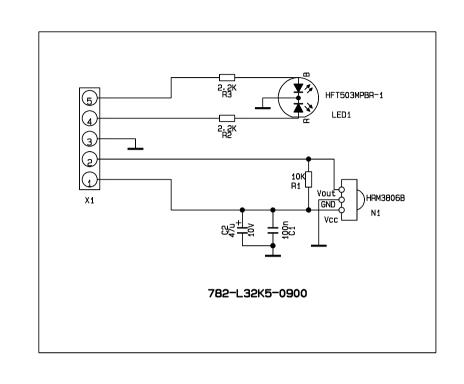


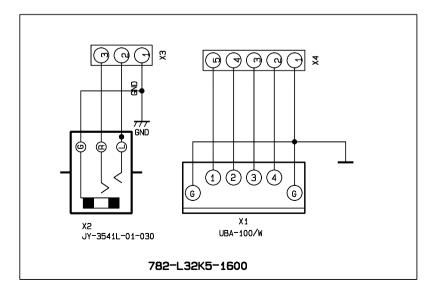
2. raster, but no picture

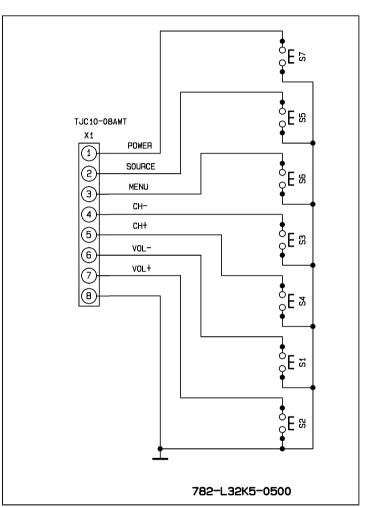


3.no sound









LC-34K5

名称:	IR&KEY&HEA	DPHONE	
设计:	yhchen	日期:20	04.11.26
第 18张	共 18 张	版本:	2.0
XOCECO 厚	门华侨电子	股份公司	研发中心

782-L32K51-5600 P4 DF14-30P-1.25H U31 DS90C383A FB43 F FB44 VCC VCC LVDS VCC 26 34 C303 AXOIN3+ vcc PLL VCC C301 C299 C300 100F 100n 100n 16V RXOIN3-12:E3 DAREDIO-71 GND T 10V DAREDO 51 13 TAO GND 12:E4 DAGRN[0-7] DARED1 52 21 RXOCKIN+ TA1 GND DARED2 54 RXOCKIN-GND 10 DARED3 53 DABLU[0-7] GND 12:E5 RX0IN2+ 12 LVDS GND 13 GND LVDS GND RXOIN1+ 49 RXCLK-LVDS GND 33 RXOIN1-FIXCLK+ PLL GND 16 35 PLL GND RXOINO+ 11 FX03-12 RXO3+ TB3 19 14 GND 3.3 NC 21 15 LVOS OPTION 19 DABLU DABLU 20 GND 23 TCO PANEL CONNECTOR DABLU 22 GND 24 TC1 DABLU 23 GND 25 TC2 VDD-LVDS DABLU 24 VDD 26 TC3 27 VDD 27 VDD 28 27 VDD 29 12.ES TC4 30 VDD 30 12:8 DARED7 - RXOCKIN+ DABLU7 TCLK-25 31 12:E6 POCLK ____ CLK IN LC-34K5 12:E6:13:A2 ENVDD ____ /PWDN 名称: LVDS Interface 日期:2004/11/26 设计: yhchen 第17 张 共 18 张 版本: 2.0

XOCECO 厦门华侨电子股份公司 研发中心

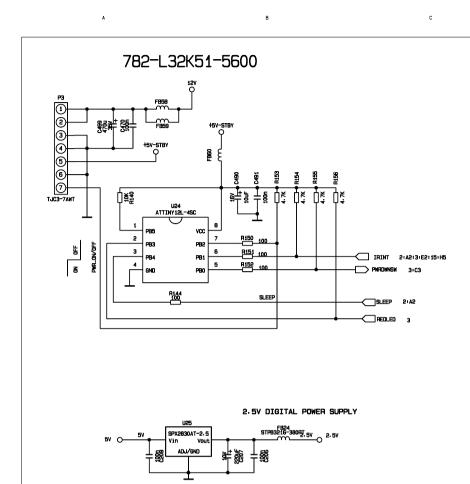
782-L32K51-5600 3 T T = N N S F F 8 2 8 2 8 2 8 3 ADCR/V1 S23P1V1 ADCR/V2 S23P1V2 ADCR/V3 S23P1V3 ADCR/V4 S23P1V4 飘飘飘 ADCR/V5 S23P1V5 ADCR/V6 \$23P1V6 10V 11+ 20F1 47uF C492 S23P1V7 S23P1Y0 ADDG/Y1 S23P1Y1 ₩ 47uF C493 ADDG/Y2 S23P1Y2 [4] [4] [4] S23P1Y3 10V 47uF C494 U30 ADDG/Y4 S23P1Y4 ADDG/Y5 S23P1Y5 MST9883B ADDS/Y6 S23P1Y6 S23P1Y7 S23P1U[7-0] ADCB/U0 S23P1U0 ADCB/U1 S23P1U1 S23P1U2 ADCB/U3 S23P1U3 ADCB/U4 S23P1U4 ADCB/U5 S23P1U5 ADCB/U6 S23P1U6 S23P1U7 S23P1CLK1 \$23P1H\$1 S23P1HS1 6+A2 S23P1H52-DE 6+A2 S23P1HS2-DE S23P1VS1 6+A2 10uF C497 U34 HEF4052BT A_SM0 3:81:A2 A_SW1 3-B2 1 LC-34K5 0 名称: HD INPUT 设计: yhchen 日期:2004/11/26 第16张 共18张 版本: 2.0 XOCECO 厦门华侨电子股份公司 研发中心

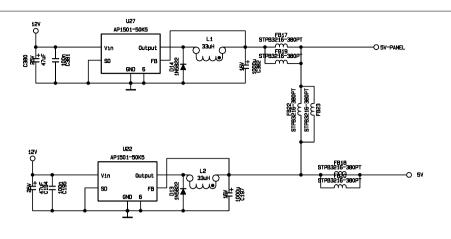
782-L32K51-5600 5V_VPC Ī 90 S23P2CLK-DECCLK 6:B1:H3 SOSBORE **⊚** a S23P2D5 00 S23P204 3-3V_VPC **9 9** R357 100 IRINT 2:A2:3:E2:14:C2 S23P2D3 **⊚** ⊜ T1.5n T100n S23P202 R355 100 SDA @ **@** S23P2D1 6 **9** AVOUT-CCD S23P2D0 ⊚ ⊜ **6** 8 8 99 2 LINE A COLOR OF COL 5239200 S23P2D[7-0] F4 S23P2D1 S23P2D2 S23P2D3 VSUPY GNDY U29 C314 | in S23P2D-VPC3230D S23P20F AV2-VIDOE S23P2DE GNDLLC VSUPLLC LLC1 S23P2CLK-DECCLK 6:B1:H4 +C232 C257 AISGND C256 1-5n 2:A4:3:C1:6:A6:9:E3:C5 3V3RESET# **→**SDA R228 4.7K R227 ____4.7K R230 750 3V3RESET# 2: A3
INT1-VCHIP# STBL2012-501 AVOUT-CCD LC-34K5 名称: TV&AV INPUT 设计: yhchen 日期:2004/11/26 第15 张 共 18 张 版本: 2.0 XOCECO 厦门华侨电子股份公司 研发中心

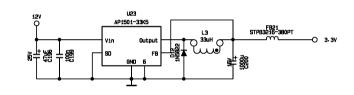
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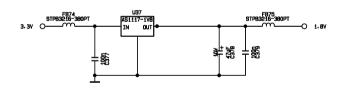
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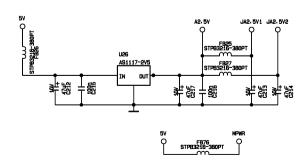
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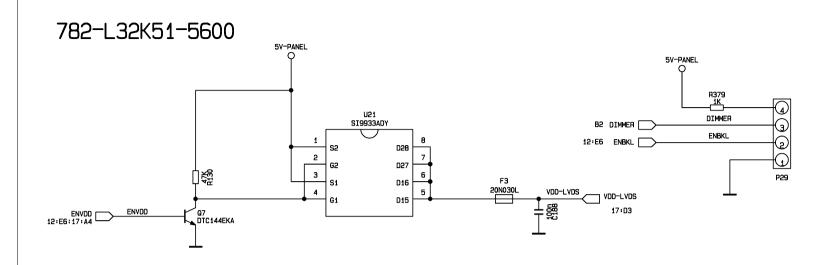


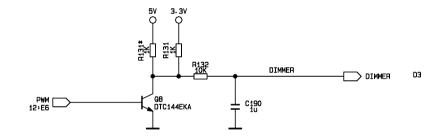




LC-34K5

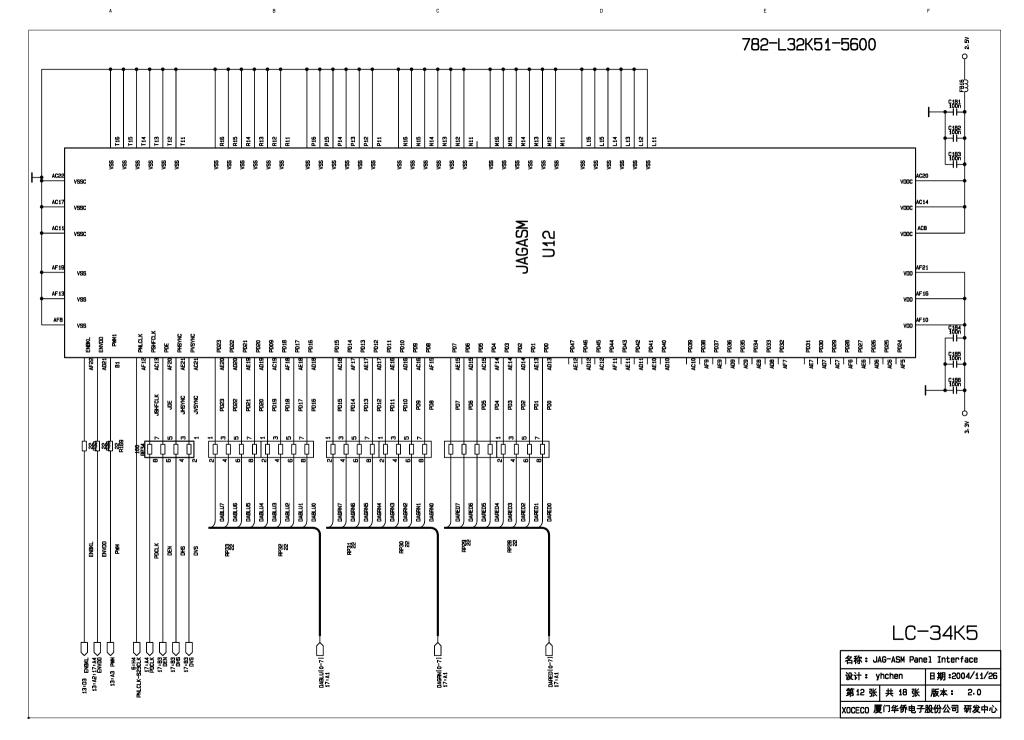
名称: Power Supply				
设计:y	hchen	日期:2004/11/26		
第14 张	共 18 张	版本:	2.0	
XOCECO 厦门华侨电子股份公司 研发中心				



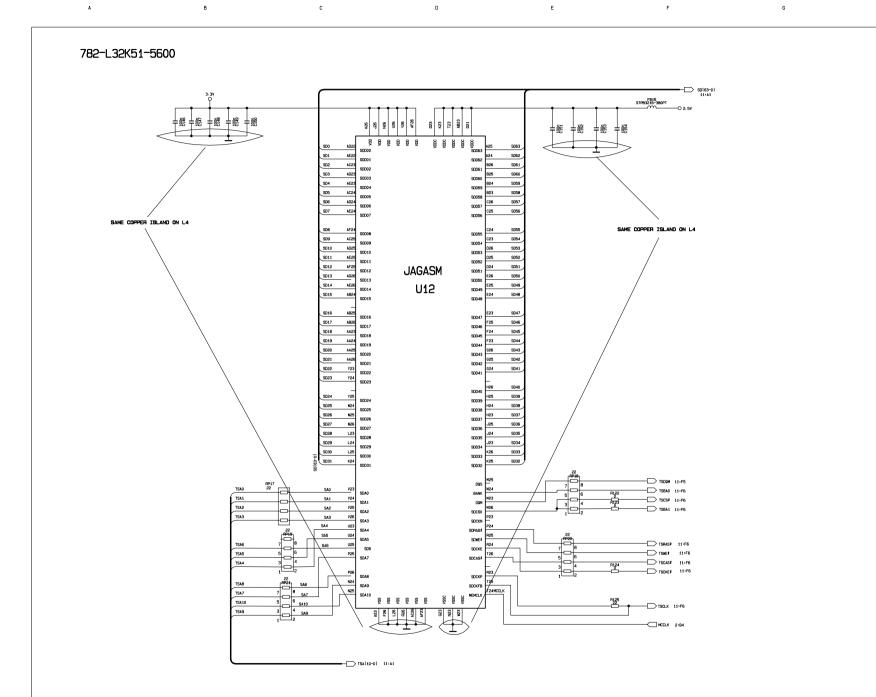


LC-34K5

名称: Panel Power and Backlight					
设计: yhc	hen	日期:20	04/11/26		
第13 张 #	+ 18 张	版本:	2.0		
XOCECO 厦门华侨电子股份公司 研发中心					

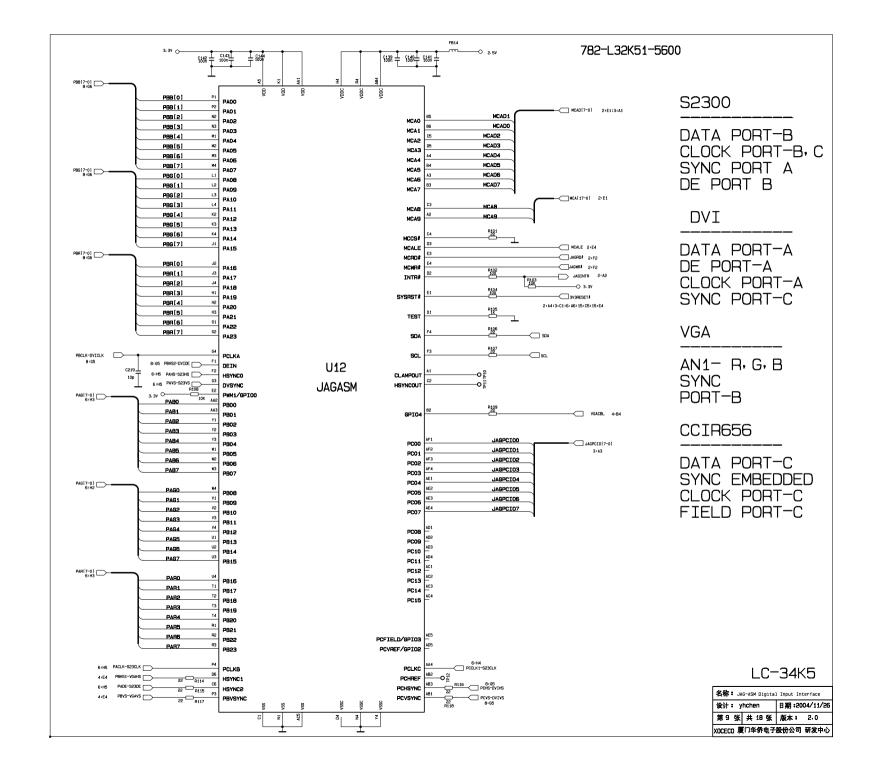


782-L32K51-5600 SORAM ALTERNATIVES FUJITSU MBS11643242A VID15 V0003 V0009 V0026 V0026 V00275 VID18 V0003 V0009 V0028 V0049 V00275 V00275 MICRON MT48LC2M3282 SAMSUNG K49643232C U19 U20 MT48LC2M32B2TG MT48LC2M32B2TG □ 輸 VSSD6 VSSD12 VSSD32 VSSD46 VSSD64 VSSD64 VSSD64 VSSS66 VSSS68 VSSS68 VSSB6 VSS72 VSSB8 8 5 8 4 LC-34K5 名称: SDRAM Frame Buffer for Jag-ASM 设计: yhchen 日期:2004/11/26 第11 张 共 18 张 版本: 2.0 XOCECO 厦门华侨电子股份公司 研发中心



LC-34K5

名称:	JAG_ASM SI	DRAM Int	erface	
设计:y	hchen	日期 :2004/11/2		
第10张	共 18 张	版本:	2.0	
XOCECO 厚	门华侨电子	股份公司	研发中	



782-L32K51-5600 62 P7 ØVI33-24+5SFW DVTDYC4N QQ0 1 9 DVIRXCOP DVIRXCON QQO 2 DVIRXCP DATESON DDC-CLK DDC-DAT PROM_SCL OVCC -FBG-TMPVCC ______ FB13 QQO 7 RB8 4.7K 0022 74 9021 R90 1K 山龗 0020 73
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000028
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000029 PCHS-DVIHS PCHS-DVIHS 9:06 PCVS-DVIVS PCVS-DVIVS 9:06 PBHS2-DVIDE PBHS2-DVIDE 9:83 PBCLK-DVICLK PBCLK-DVICLK 9:A3 TNDS16 TMDS17 PBR[6] TNDS18 PBR[5] PROM_SCL TNDS19 PBR[4] PROM_SDA PP16 22 PBR(3) TNDS21 PBR[2] PBR[1] PBR[0] LC-34K5 ——— PBR[7-0] 9:A2 → PBG[7-0] 9:A2 PBB[7-0] 9+A1 名称: DVI Interface DVIRXOE# 3:81 设计: yhchen 日期:2004/11/26 SCDT 2: A2 第8张 共18张 版本: 2.0 DVIRXPD 3:81 XOCECO 厦门华侨电子股份公司 研发中心

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782-L32K51-5600 FB41 23SDA[10-0] 6:H6 3.3VSDRAM2 3. 3VSDRAM1 9 9 9 23SDD0 23SDD1 23SDD2 23SDA0 23SDA1 23SDD3 27 23SDA2 23SDA3 60 235004 23SDD5 DQ5 23SDA4 23SDD6 23SDA5 62 23SDD7 23SDA6 63 64 23SDD8 23SDD9 23SDA8 65 23SDD10 DQ10 66 23SDA9 79 23SDD11 3. 3V DQ11 24 23SDD12 DQ12 23SDD13 14 21 30 57 69 70 DQ13 83 23SDD14 U14 DQ14 23SDD15 DQ15 MT48LC2M32B2TG 23SDD16 DQ16 33 23SDD17 DQ17 34 23SDD18 DQ18 23SDD19 23SDD20 PLL1.8V 23SDDQM D020 71 2350021 6:H6 DQ21 28 23SDD22 DECOUPLING FOR FLI2310 59 42 2350023 DQ23 55 23SDD24 6:H6 23SDBA0 23SDBA0 DQ24 23SDD25 6:H6 23SDBA1 ______23SDBA1 23SDD26 DQ26 50 23SDD27 6:H6 23SDWE# 23SDWE# D027 6:H6 23SDCAS# 23SDCAS# 23SDD28 23SDD29 6:H6 23SDCS# 23SDCS# DQ29 68 54 23SDD30 23SDCLK 23SDCLK DQ30 23SDD31 LC-34K5 VSS VSS 名称: SDRAM for FIL2310 设计: yhchen 日期:2004/11/26 第7张 共18张 版本: 2.0 23SDD[31-0] XOCECO 厦门华侨电子股份公司 研发中心

- 18 A ## ## # # # 782-L32K51-5600 —O DAC1.8V 中國 日本 日本 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 | 100 -O PLL1.8V PAG[7-0] 9:A4 E/Y/Y_0UT_7 155 2 VSYNC1_PORT1 \$23P1VS1 G/Y/Y_OUT_6 154 PASS FIELD ID: PORT: 6/Y/Y_0UT_5 PAS5 16+H4 S23P1CLK1 IN.CLK1.PORT1 5 HSYNC2_PORT1 6/Y/Y_0UT_4 152 PAS4 16+H4 S23P1HS2-DE 6 VSYNC2_PORT1
7 FIELD ID2_PORT1
8 VDD1(3.3) 6/Y/Y_0UT_3 PAS3 6/Y/Y_0UT_2 PAS2 6/Y/Y_0UT_1 PAS1 6/Y/Y_OUT_0 VSS10 | SUBSTANCE | SUBS V00e(3.3) R/Y/Pr_0UT_7 16-H3 S23P1U[7-0] R/Y/Pr_OUT_6 PAR6 R/Y/Pr_0UT_5 S23P1U1 PAR5 16:12 S23P1Y(7-0) R/Y/Pr_0UT_4 82301112 PARA R/Y/Pr_0UT_3 R/Y/Pr_0UT_2 YSScore S23P1U3 PAR3 S23P1U4 PARP S23P1U5 PAR1 VSScore VDDcore7(1.8) 137 R/Y/Pr_0UT_1 136 R/Y/Pr_0UT_0 135 8/U/b_0UT_7 S23P1U6 S23P1U7 U13 523P1V1 B/U/b.0UT.5 B/U/b.0UT.5 133 FLI2310 S23P1V3 PABS B/U/b_0UT_4 B/U/b_0UT_4 B/U/b_0UT_3 S23P1V5 PAB3 B/U/b_0UT_2 VSS10 \$23P1V6 \$23P1V7 VSS10 V007(3.3) B/U/b.0UT.1 B/U/b.0UT.0 CLKOUT VSScore S23P1Y0 - RISS \$23P1Y1 PCCLK1-523CLK 9:06 S23P1Y2 _% V00core6(1.8) PACLK-S23CLK 9:A6 \$23P1Y3 CTLOUT4 121 S23P1Y4 –o⊭ -o≝ PAGE-SZ3DE 9-A6 CTLOUTS 120 CTLOUT1 119 S23P1Y5 PAVS-523VS 9-83 CTLOUTO 118 \$23P1Y6 TEST OUT1 117 TEST OUTO 115 -o≝ 20— SDRAM CLKIN 2350CLK 44 DEV.ADDRO SCLK 2350CLK H5:7:A4 VSS10 112 VDS6(3.3) 111 46 SDATA SDRAM CLKOUT SDA 78 2350CLK SDA 🗁 23500.K H5:7:A4 47 REST_N SOMM CANOTI 110
SOMM ON 110
SOMM ON 100
SOMM BAD 100
SOMM 538000H 235000M 7:A3 49 VSS10 SDRAM DO 23SDCS# _____2390CB# 2390840 ______2350BA0 23906A1 ______23SDBA1 51 SDRAW D1 239DCAS# 7:44 -C3SDCAS# -C3SDRAS# 23SDHET 7:M 2350A[10-0] 7:A1 23600(31-0) 7.45 山窯 山窯 DEVADOR1 235044 235044 235042 235042 235042 23SDA [10-0] LC-34K5 2350026 2350027 2350028 2350030 2350031 235004 235004 235006 235007 235008 235009 23500 2350 2380013 2380014 2380017 2380019 2380021 2380022 2380023 2380024 2380026 2380026 23800[31-0] 名称: FLI2310 De-Interface 设计: yhchen 日期:2004/11/26 第6张 共18张 版本: 2.0

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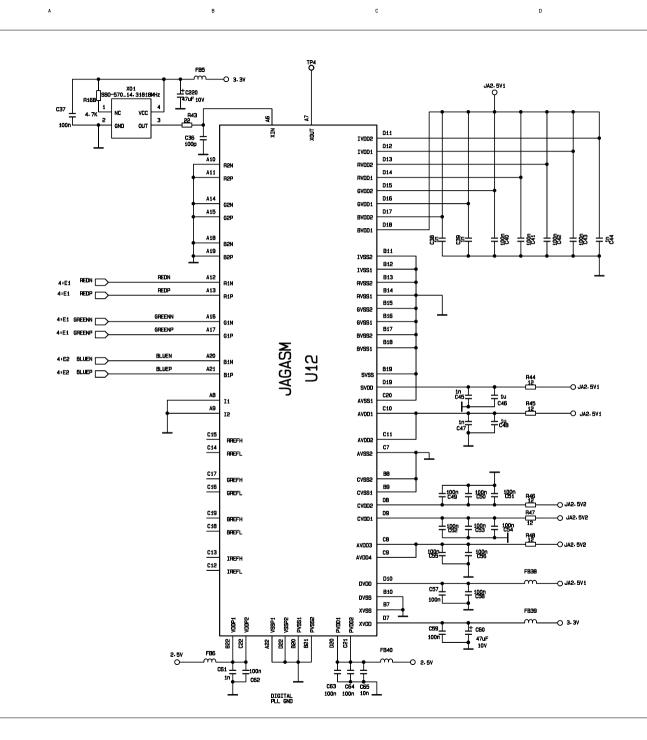
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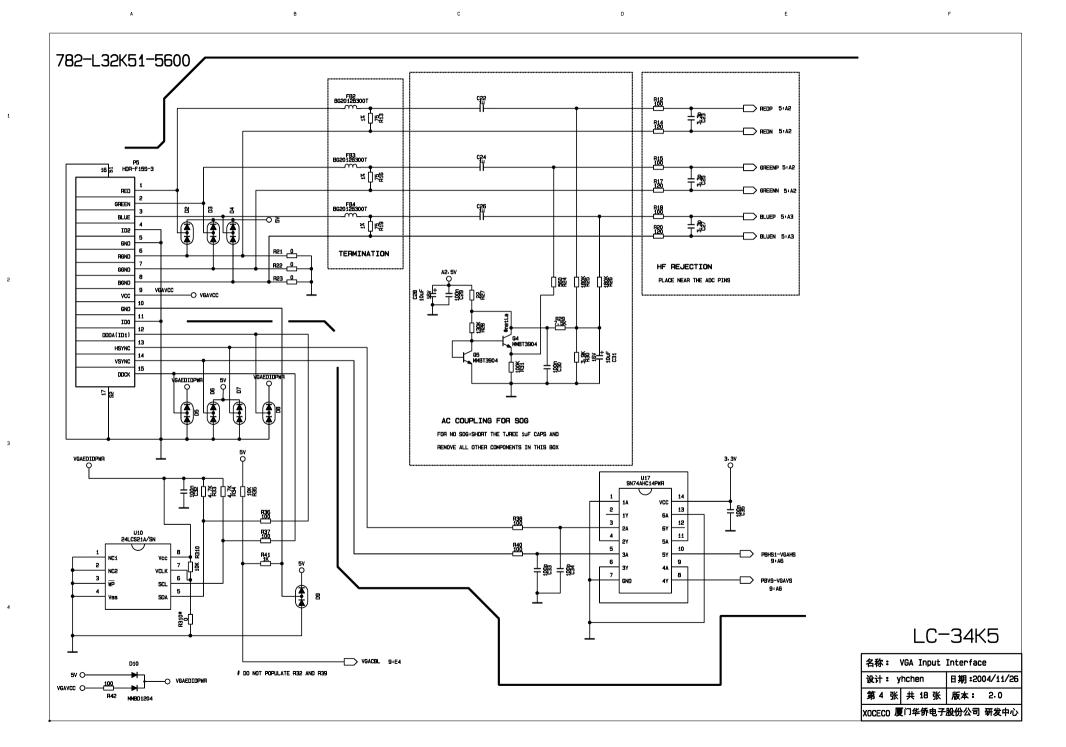
XOCECO 厦门华侨电子股份公司 研发中心

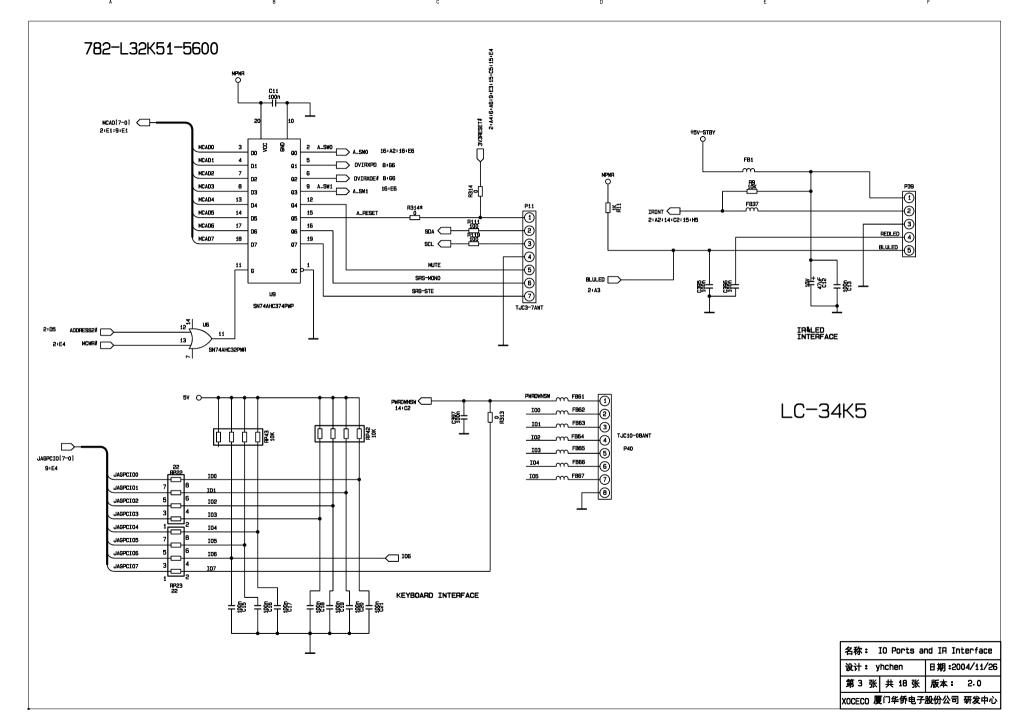
782-L32K51-5600

LC-34K5

名称: JAG-ASM ANALOG INTERFACE				
设计: yhchen	日期:2004/11/26			
第5张 共18张				
XOCECO 厦门华侨电子股份公司 研发中				



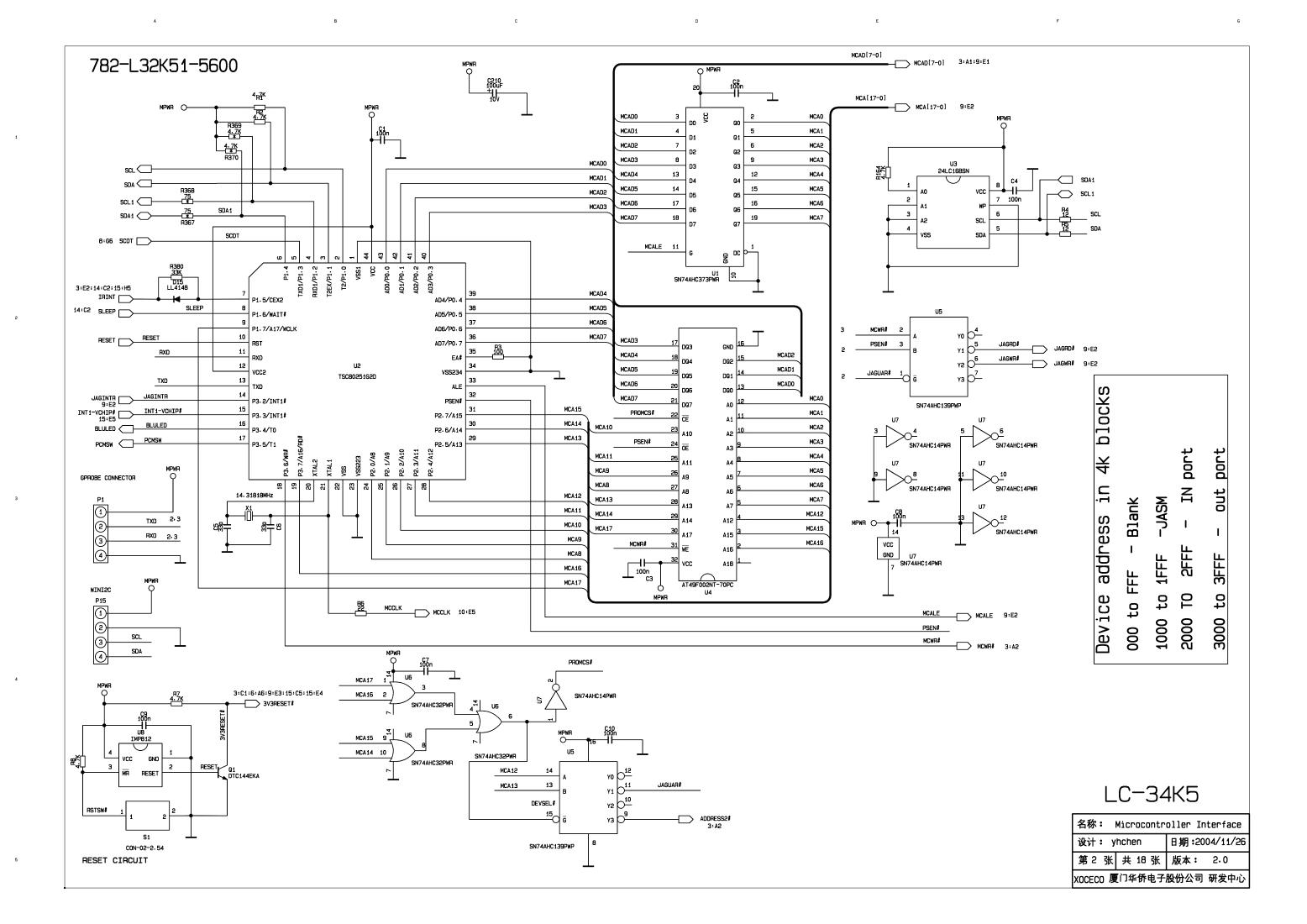


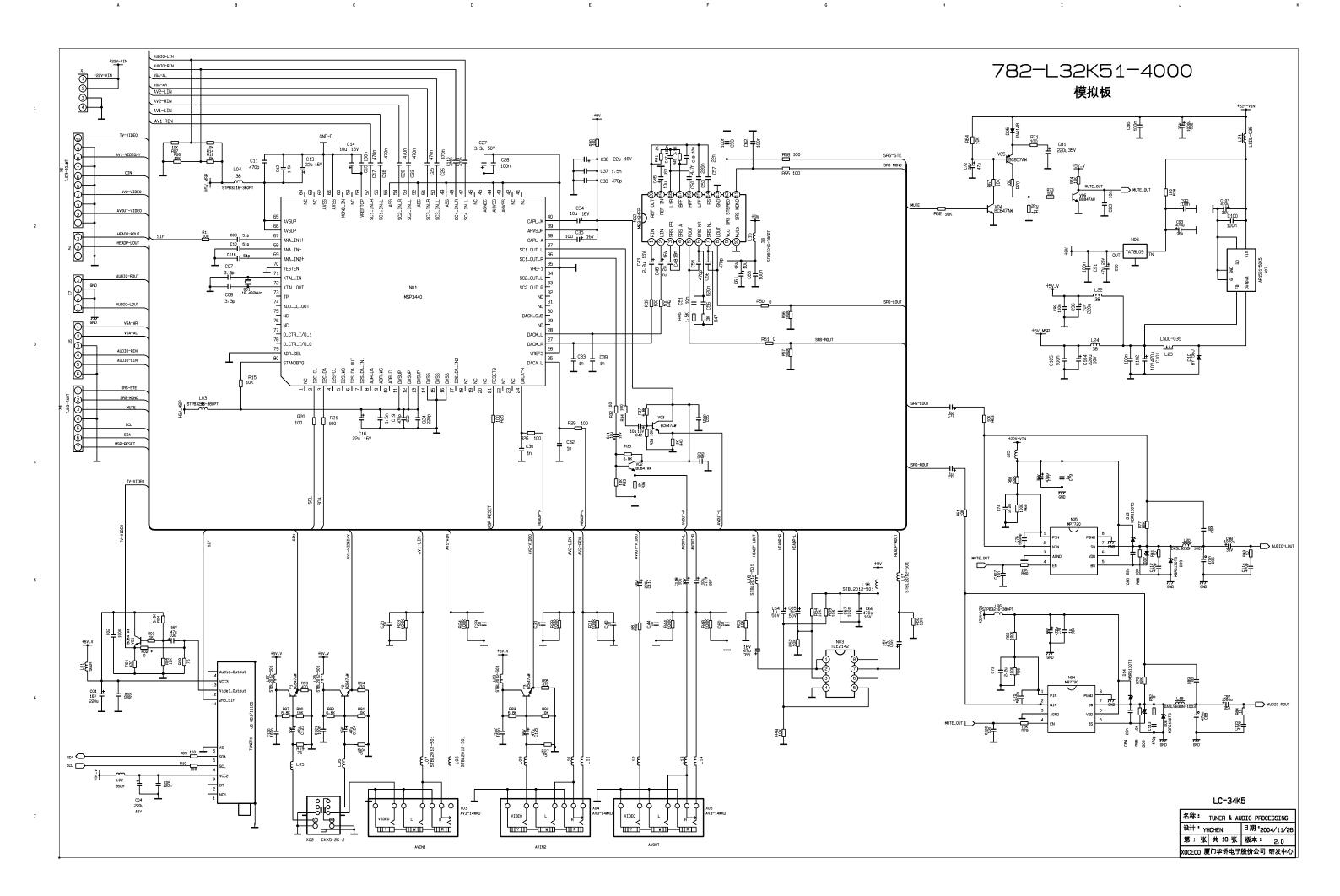


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